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Age: 34 y.o.

Felipe Nicolás Diniello

Current Job

Globant

2017 - 2022 **IoT Studio**, *Buenos Aires, Argentina*

Position: IoT Edge Engineer (SME.)
Team size: 4 SW and FW engineers
Duties:
Felipe excecuted as a Team Leader, and was in charge of the solution Architecture and definition of the whole flow of the process and different build Stages of a Linux based distribution for a device. As part of the automation process he took in charge the individual Firmware images build process and replaced the Eclipe environment dependencies and migrated onto a containerized cross toolchain able to integrate with Jenkins. *Used tools:*

- Git, GitHub and Gitflow
- Jenkins for CI/CD
- Docker
- Microcontrollers and SoC (ARM and Blackfin)
- Embedded Linux OS (Yocto and Buildroot)
- C/C++, Makefile, CMake and crosscompiling.

Technical Skills

Programing

- Advanced knowledge on **C/C++**. OOP, Templates y Design Pattern implementation.
- Intermediate knowledge on **Rust** programming language.
- Experience with **VHDL** for **FPGAs**
- Experience with Crosscompilations and build systems such as **Make** and **GCC toolchain**
- Experience with **Matlab** for:
  - Systems Simulations and Control Systems
  - HIL Systems Simulations (Hardware In the Loop)
- UNIX shell and BASH tools for scripting (sed, awk, grep, etc).

CAD Tools

- Experience with PCB design tools: **Altium** and **KiCAD**
- Advanced experience with parametric 3D CAD **FreeCAD**
- Experience with 3D printing: **Cura**.

Qualifications

2019 **Electronic Engineer**, *Universidad Tecnológica Nacional - FRBA*, Buenos Aires

Languages

Spanish, *native*

English, *advanced*

French, *basic*

Prior Experience

CITEDEF, Defense Department

2016 - March 2017 **Scholarship**, *Digital Technics Lab.*, Applied Electronics Department, Villa Martelli - Buenos Aires
Project: Argentinian Air Target - Ground Segment
Objectives: Develop a low cost resynchronization platform for telemetric payloads coming from an Unmanned Aerial Vehicle.
Keywords: FPGA, VHDL, DSP, HIL, Simulation.

Facultad Regional Buenos Aires - Tenaris

2014 - 2018 **Intern**, *Science, Technology and Production Secretary*, UTN-FRBA
Project: Field-Joint-Coating
Objectives: Develop an electronic system capable of heating large copper weaves with a temperature profile to weld plastic surfaces.
Keywords: PID Controller, Power Electronics, DSP, Electronic Front End.

CITEDEF, Defense Department

2011 - 2013 **Intern**, *Pyrotechnics Lab.*, Applied Chemistry Department
Project: TINoC (Tornillo Iniciador Normalizado Codificado) [Translates to: Normalized Codified Fire-Started]
Objectives: Develop a safe communication protocol for a microcontroller to trigger the initiazion sequence for solid-fuel rockets only after the correct code is entered.
Keywords: ARM, C, Crosscompilation, Linux

College Activity

Teaching

2017 - 2019 **Auxiliary**, *Control Systems*, Facultad Regional Buenos Aires - UTN
Head professor: Sp. Emilio Ciccolella
Subjects: Systems modeling, Controllers design, Modern Control.

2014 - 2018 **Auxiliary**, *Programming I*, Facultad Regional Buenos Aires - UTN
Head professor: Eng. Mariana Prieto Canalejo
Subjects: C programming language under GNU/Linux environments.

Awards and Special Mentions

2010 **Projects Fair**, *UTN-FRBA*
Project: Three Axis CNC Controller
Category: Assignments
Award: Third Place

2017 **Innovar Fair**, *Ministry for Science and Technology*
Project: Autonomous Navigation Platform
Category: Innovative Product and Design